APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/659,848	09/11/2003	Ruei-chin Luo	24061.78	5534

			EXAMINER
42717	7590	07/27/2006	KENDALL, CHUCK O

HAYNES AND BOONE, LLP
901 MAIN STREET, SUITE 3100
DALLAS, TX 75202

ART UNIT	PAPER NUMBER
2192	

DATE MAILED: 07/27/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)
Office Action Summary	10/659,848	LUO ET AL.
	Examiner	Art Unit	
	Chuck O. Kendall	2192	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE <u>3</u> MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on <u>11 September 2003</u>.

2a)☐ This action is **FINAL**.   2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) <u>1-20</u> is/are pending in the application.

    4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) <u>1 - 9, 11,13 - 16, 19 and 20</u> is/are rejected.

7)☒ Claim(s) <u>10 and 17</u> is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on <u>11 September 2003</u> is/are: a)☒ accepted or b)☐ objected to by the Examiner.

    Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

    Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

    a)☐ All   b)☐ Some * c)☐ None of:

      1.☐ Certified copies of the priority documents have been received.

      2.☐ Certified copies of the priority documents have been received in Application No. _____.

      3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

    * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1)☒ Notice of References Cited (PTO-892)

2)☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)

3)☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____.

4)☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____.

5)☐ Notice of Informal Patent Application (PTO-152)

6)☐ Other: _____.

Detailed Action

1.   This action is in response to Application filed 09/11/2003.

2.   Claims 1 – 20 have been examined and are pending.

Claim Rejections - 35 USC § 103

3.   The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

4.   Claims 1 – 9,11,13 – 16,19 and 20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Barroso et al. US2002/0046324 A1 in view of Zizzo USPN 6,578,174 B2.

Regarding claims 1,13 and 19 Barroso discloses a method for accessing a plurality of memory compiler units, the method comprising:

prompting, via a multi-compiler interface, for a selection of a first memory compiler unit from a plurality of memory compiler units ([0036], see for each L2 module 130 is a memory controller 131 and see "directly interfaces to one bank of up to 32");

remotely linking to the selected first memory compiler unit ([0036], see remote engine (RE)141);

Barroso doesn't expressly disclose generating a combination datasheet comprising a plurality of memory instances.

However, Zizzo in an analogous art and similar configuration of circuit designing and interfacing discloses collecting additional information regarding selected parts and displaying them as data sheets (10:55 – 60). Therefore it would have been obvious to one of ordinary skill in the art at the time the invention was made to combine Barroso and Zizzo because, it would enable displaying selected information about the system.

Regarding claim 2, the method of claim 1 further comprising:

prompting, via the multi-compiler interface, for a selection of a second memory compiler unit from the plurality of memory compiler units (Barroso see, [0052] for Second-level Cache, L2 banks (second memory) and private memory controller (compiler unit));

remotely linking to the selected second memory compiler unit (Barroso, [0036], see remote engine (RE)141, also see 0058, which shows triggering remote requests between L2 banks (second memory)); and

generating the combination datasheet comprising a plurality of memory instances created by both the first and second memory compiler units (Zizzo, 10:55 – 60).

Regarding claims 3 and 15, Zizzo further discloses the method of claim 2 further comprising:

displaying the combination datasheet on a web page (Zizzo, 7:10 – 15).

Regarding claim 4, the method of claim 1 wherein the plurality of memory

compiler units are provided on separate servers (Barroso [0100], shows each client

process has a separate server).

Regarding claims 5 and 20, the method of claim 1 further comprising providing

the plurality of memory compiler units on at least two separate servers (Barroso [0100],

shows each client process has a separate server).

Regarding claim 6, the method of claim 1 further comprising providing the multi-

compiler interface and the plurality of memory compiler units on at least two separate

servers (Barroso [0100], shows each client process has a separate server).

Regarding claim 7, the method of claim 1 wherein the prompting comprises:

requesting a user to select a link to the first memory compiler unit (Barosso, see

[0069] for request, send and receiving replies).

Regarding claim 8, the method of claim 1 wherein remotely linking to the selected

first memory compiler unit comprises:

displaying a web page associated with the selected first memory compiler unit (Zizzo, 7:10 – 15).

Regarding claims 9 and 16, the method of claim 1 wherein remotely linking to the selected first memory compiler unit comprises:
soliciting inputs to one or more parameters through a web-based screen display (Zizzo,10:10 – 20, see internet and portal site).

Regarding claim 11, method of claim 1, wherein the remotely linking to the selected first memory compiler unit comprises creating a footprint based on the inputs and displaying the footprint on a web page (Barroso, 0007, see footprint).

Regarding claim 14, the computer readable medium of claim 12 further comprising providing a plurality of memory compiler units accessible through a multi-compiler interface ([0036], see for each L2 module 130 is a memory controller 131 and see "directly interfaces to one bank of up to 32").

5.   Claims 12 is rejected under 35 U.S.C. 103(a) as being unpatentable over Barroso et al. US2002/0046324 A1 in view of Zizzo USPN 6,578,174 B2 as applied in claim 1 and further in view of Watanabe et al. USPN 6,157,947.

Regarding claim 12, Barroso as modified by Zizzo discloses all the claimed limitations as disclosed in claim 1 above. The combination of Barroso and Zizzo doesn't expressly wherein remotely linking to the selected first memory compiler unit comprises storing the design files on a FTP server. However, Watanabe discloses in an analogous art and similar configuration transferring data between servers and the internet using FTP, further stating that FTP is a standard file transferring protocol (7:30 – 35).

Therefore it would have been obvious to one of ordinary skill in the art at the time the invention was made to combine Barroso and Zizzo with Watanabe because, it would enable transferring files between servers and the internet.

Regarding claim 18, the method of claim 12 further comprising displaying a footprint on a web page (Barroso, 0007, see footprint).

Allowable Subject Matter

6.   Claims 10 and 17 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

"...calculating memory instance ratios based on the inputs and displaying the memory instance ratios on a web page".

Correspondence information

7.   Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Chuck Kendall whose telephone number is 571-272-

3698. The examiner can normally be reached on 10:00 am - 6:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Tuan Dam can be reached on 571-272-3695. The fax phone number for the

organization where this application or proceeding is assigned is **571-273-8300**.

Information regarding the status of an application may be obtained from the

Patent Application Information Retrieval (PAIR) system. Status information for

published applications may be obtained from either Private PAIR or Public PAIR.

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For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

Ck.

Chuck Kendall   07/24/06